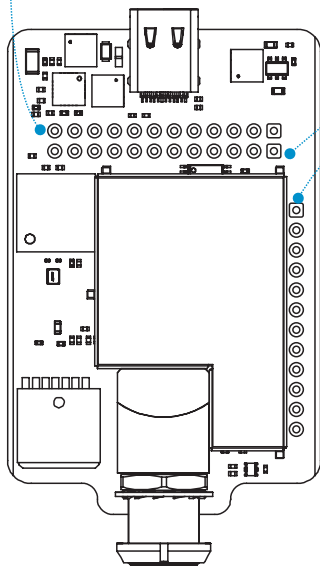


CON2			CON3			
CON2_1	TXD	UART Transmit	⊠ ⊠	CON3_1	CTS	UART Clear To Send
CON2_2	RXD	UART Receive	⊙ ⊙	CON3_2	RTS	UART Ready to Send
CON2_3	E_UNIPOLAR_OUT		⊙ ⊙	CON3_3	SDA_EXT	I2C SDA
CON2_4	I_BIPOLAR_OUT		⊙ ⊙	CON3_4	SCL_EXT	I2C SCL
CON2_5	D5	Digital GPIO	⊙ ⊙	CON3_5	Reserved	
CON2_6	DGND	GND, digital zone	⊙ ⊙	CON3_6	Reserved	
CON2_7	RESET	Active low reset	⊙ ⊙	CON3_7	E_SET	External Voltage Setpoint
CON2_8	DWNLD	Active low FW update	⊙ ⊙	CON3_8	3.3VD_SHDN <sup>1</sup>	
CON2_9	ADC_IN	Auxiliary ADC input	⊙ ⊙	CON3_9	D6	Digital GPIO
CON2_10	DAC_OUT	Auxiliary DAC output	⊙ ⊙	CON3_10	3.3V_OUT	3.3V Power Supply <sup>2</sup>
CON2_11	DGND	GND, digitale zone	⊙ ⊙	CON3_11	VPST-	Potentiostat -V Rail <sup>3</sup>
CON2_12	5V_IN	5V Power Input	⊙ ⊙	CON3_12	VPST+	Potentiostat +V Rail <sup>3</sup>



CON1			
CON1_1	D3	Digital GPIO	⊠ ⊠
CON1_2	D2	Digital GPIO	⊙ ⊙
CON1_3	D1	Digital GPIO	⊙ ⊙
CON1_4	D0	Digital GPIO	⊙ ⊙
CON1_5	5V_IN	5V power input	⊙ ⊙
CON1_6	DGND	GND, digital zone	⊙ ⊙
CON1_7	D4	Digital GPIO	⊙ ⊙
CON1_8	SENSE	Sense Electrode	⊙ ⊙
CON1_9	AGND	GND, analog zone	⊙ ⊙
CON1_10	WE	Potentiostat WE	⊙ ⊙
CON1_11	RE	Potentiostat RE	⊙ ⊙
CON1_12	CE	Potentiostat CE	⊙ ⊙

- <sup>1</sup> 3.3VD\_SHDN: Active low Shutdown. Internally pulled up to 5V with 100K resistor. Pulling to Gnd disables all power on the ES4.
- <sup>2</sup> Maximum draw of 50mA from this pin.
- <sup>3</sup> Maximum draw of 20mA from this pin.