EmStat$4M$™

CON2

CON2_1	TXD	UART Transmit	◙ ◙
CON2_2	RXD	UART Receive	◎ ◎
CON2_3	E_UNIPOLAR_OUT		◎ ◎
CON2_4	I_BIPOLAR_OUT		◎ ◎
CON2_5	D5	Digital GPIO	◎ ◎
CON2_6	DGND	GND, digital zone	◎ ◎
CON2_7	RESET	Active low reset	◎ ◎
CON2_8	DWNLD	Active low FW update	◎ ◎
CON2_9	ADC_IN	Auxiliary ADC input	◎ ◎
CON2_10	DAC_OUT	Auxiliary DAC output	◎ ◎
CON2_11	DGND	GND, digitale zone	◎ ◎
CON2_12	5V_IN	5V Power Input	◎ ◎

CON3

CON3_1	CTS	UART Clear To Send	◙ ◙
CON3_2	RTS	UART Ready to Send	◎ ◎
CON3_3	SDA_EXT	I2C SDA	◎ ◎
CON3_4	SCL_EXT	I2C SCL	◎ ◎
CON3_5	Reserved		◎ ◎
CON3_6	Reserved		◎ ◎
CON3_7	E_SET	External Voltage Setpoint	◎ ◎
CON3_8	3.3VD_SHDN[1]		◎ ◎
CON3_9	D6	Digital GPIO	◎ ◎
CON3_10	3.3V_OUT	3.3V Power Supply[2]	◎ ◎
CON3_11	VPST-	Potentiostat -V Rail[3]	◎ ◎
CON3_12	VPST+	Potentiostat +V Rail[3]	◎ ◎
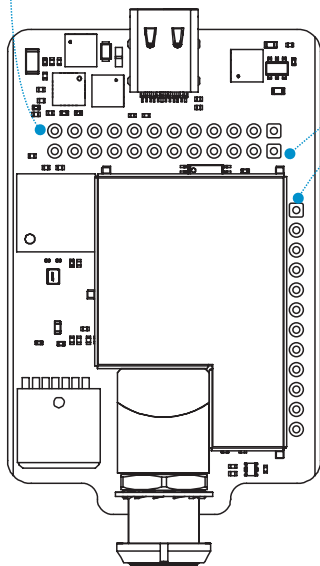
CON1

CON1_1	D3	Digital GPIO	◙
CON1_2	D2	Digital GPIO	◎
CON1_3	D1	Digital GPIO	◎
CON1_4	D0	Digital GPIO	◎
CON1_5	5V_IN	5V power input	◎
CON1_6	DGND	GND, digital zone	◎
CON1_7	D4	Digital GPIO	◎
CON1_8	SENSE	Sense Electrode	◎
CON1_9	AGND	GND, analog zone	◎
CON1_10	WE	Potentiostat WE	◎
CON1_11	RE	Potentiostat RE	◎
CON1_12	CE	Potentiostat CE	◎

[1] 3.3VD_SHDN: Active low Shutdown.
Internally pulled up to 5V with 100K resistor.
Pulling to Gnd disables all power on the ES4.
[2] Maximum draw of 50mA from this pin.
[3] Maximum draw of 20mA from this pin.

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